

## Resources

### MicroDesign Report Reveals Embedded Chips

MicroDesign Resources, the publisher of *Microprocessor Report*, has just released a special report covering embedded microprocessors. Titled ***Selecting a High-Performance Embedded Microprocessor*** and written by *Microprocessor Report* editor Jim Turley, the 450-page volume examines more than 100 different 32-bit embedded chips. Included are technical reviews and comparisons of x86 and 680x0 processors as well as PowerPC, MIPS, i960, and a number of other chip families, both famous and obscure, offered by more than a dozen vendors worldwide. The book includes chapters devoted to each architecture and concludes with estimated manufacturing cost information and tables of comparative performance.

Individual copies of the report are priced at \$995; additional copies are \$295 apiece. For more information, or to place an order, contact MDR (Sebastopol, Calif.) at 800.527.0288 or 707.824.4004; fax 707.823.0504; access the Web at [www.chipanalyst.com](http://www.chipanalyst.com).

### Boston Goes Virtual

Billed as North America's largest virtual reality and VRML event, **Virtual World '95** will take place in Boston's World Trade Center from November 29 through December 1. The show includes a two-day exhibition and a three-day conference covering topics as varied as medicine, museums, the Internet, and "desktops (immersive and nonimmersive)."

Admission pricing is a la carte, with conference sessions and workshops priced separately. Attendees spending more than \$295 receive the complete conference proceedings on CD-ROM. For more information, or to register, contact Mecklermedia (Westport, Conn.) at 800.632.5537; fax 203.226.6976; or send e-mail to [vrconf@mecklermedia.com](mailto:vrconf@mecklermedia.com).

### DRAM Changes Charted in Palo Alto

Steven Przybylski will present ***The Changing DRAM and VRAM Marketplace***, a full-day seminar, on November 28. Przybylski examines major DRAM architectures for main memory and graphics, discusses the inadequacy of standard DRAMs for many applications, and provides an analysis of the DRAM marketplace.

Registration costs \$595 per person, with discounts for groups of four or more. For more information, or to register, contact Dr. Przybylski (San Jose, Calif.) at 408.984.2719, or send e-mail to [sp@acm.org](mailto:sp@acm.org).

### Microarchitecture Is Big in Michigan

The 28th annual IEEE **International Symposium on Microarchitecture** is being held this year in Ann Arbor (Mich.) November 29–December 1. IBM Vice President Richard Baum will deliver the keynote address; Tom Jermoluk of SGI and Intel's Fred Pollack are also invited speakers. In addition to the conference's traditional focus on parallelism, presentations will cover branch prediction, data flow and multithreading, and object code translation. Also new this year are three no-cost tutorials to be held on the day following the conference.

Admission to the conference runs \$345 for IEEE members; \$430 for nonmembers. To register, fax UM at 313.763.4617 or send e-mail to [micro28@cs.ucdavis.edu](mailto:micro28@cs.ucdavis.edu).

### Smart Card Show in San Jose

Those ubiquitous credit cards with embedded microcontrollers are coming to San Jose (Calif.) November 29–30. The **CardTech/SecurTech '95 West** show will be held in the San Jose Convention Center, and will feature two seminar tracks and 30 speakers.

Admission costs \$295 for a single day or \$495 for both days. To register, contact CardTech (Rockville, Maryland) at 800.442.2878; fax 301.881.2430.