

Literature Watch

ASICs

Designers search for the secret to ease ASIC migration. Designers are migrating from one technology to another—from FPGA to gate array, gate array to standard cell, PLDs to standard cell, and so on. Barbara Tuck Egan; Computer Design, 12/91, pg 78, 11 pgs.

Buses

SBus: an open bus architecture. Rudolf Usselman, Sparc International; IEEE Micro, 12/91, pg 80, 4 pgs.

SCSI-3 to pick up where SCSI-2 leaves off. Warren Andrews; Computer Design, 12/91, pg 44, 4 pgs.

Development Tools

New emulator rounds out support for AM29200. Tom Williams; Computer Design, 12/91, pg 40, 2 pgs.

Schematics battle equations for design representation. Ray Weiss; EDN, 12/19/91, pg 62, 6 pgs.

Miscellaneous

8051 family gets second wind; new versions extend life of classic μ C. Ray Weiss; EDN, 12/19/91, pg 52, 2 pgs.

Arm yourself with LAN know-how. Dan Strassberg; EDN, 12/19/91, pg 130, 8 pgs.

Delay lines take on timing tasks. Tom Ormond; EDN, 12/19/91, pg 108, 5 pgs.

Designing neural networks commands skill and savvy. Neural-network simulators that run on PCs can serve educational purposes as well as host real-world applications. Maury Wright; EDN, 12/5/91, pg 86, 6 pgs.

IEEE's Posix: making progress. Seven more standards are near completion in this cornerstone of the international open system software effort. D. Richard Kuhn, Nat'l Inst.-Standards & Tech.; IEEE Spectrum, 12/91, pg 36, 4 pgs.

Intel sues AMD for PLA copyright infringement. Richard H. Stern; IEEE Micro, 12/91, pg 4, 2 pgs.

Microprocessors drive chip recovery. Julie Handelman; Electronic Business, 12/9/91, pg 25, 1 pg.

Out with the old? Quick-stepping companies are challenging the stalwarts in lucrative auto market. Francis J. Lavoie; Electronics, 12/91, pg 39, 2 pgs.

The business of finding the best battery. Richard A. Quinnell; EDN, 12/5/91, pg 162, 5 pgs.

Peripheral Chips

High-resolution ADCs tailored to system needs. Jeffrey Child; Computer Design, 12/91, pg 107, 6 pgs.

Processors

EDN's 18th annual μ P/ μ C chip directory. Michael Markowitz; EDN, 11/21/91, pg 82, 48 pgs.

Highly integrated RISC CPUs simplify system design, deliver 60-90+ MIPS. Dave Bursky; Electronic Design, 12/19/91, pg 27, 2 pgs.

RISC processors dressed up for embedded applications. Dave Wilson; Computer Design, 12/91, pg 34, 3 pgs.

Smart peripherals make or break microcontrollers. Charles H. Small; EDN, 12/5/91, pg 124, 6 pgs.

System Design

A parallel, scalable, microprocessor-based database computer for performance gains and capacity growth. David K. Hsiao; IEEE Micro, 12/91, pg 44, 17 pgs.

Add-on boards boost real-time performance. Add-on boards can increase performance, but their use still isn't straightforward. Warren Andrews; Computer Design, 12/91, pg 71, 5 pgs.

VLSI accelerators for large database systems. Kuo Chu Lee, Takako Matoba Hickey, Victor W. Mak, Gary E. Herman, Bellcore; IEEE Micro, 12/91, pg 8, 13 pgs.