

Recent IC Announcements

Part Number	Vendor	Description	Price/Quantity	Availability
Embedded Microprocessors				
MC68PM302	Motorola 512.891.3823	New 68302-based communications controller has PCMCIA interface, low-power mode; suitable for PCMCIA wireless LAN or modem card.	\$17.50/10,000	Prod.—Now
68HM360	Motorola 512.891.3823	Enhanced 68360 QUICC controller supports up to 32 HDLC channels on one serial channel, suitable for T1 and E1 applications.	\$57.40/10,000	Prod.—Now
V851	NEC 800.366.9782	32-bit RISC processor is now available in U.S. Chip offers up to 29 MIPS at 25 MHz, on-chip RAM, and masked or programmable ROM.	\$19/1	Samples—Now Prod.—2Q95
IDTR3071 IDTR3081	IDT 800.345.7015	Two MIPS-based processors get speed upgrades to 50 MHz, are pin-compatible with existing R3051 line.	\$53/10,000	Prod.—Now
Am188EM	AMD 408.732.2400	Microcontroller is compatible with 80C186 with 8-bit data bus, demultiplexed address bus, and 40-MHz peak operating frequency.	\$8/10,000	Prod.—Now
Interface				
AD807	Analog Devices 617.937.1428	Single-chip back end for fiber-optic ATM and SONET receivers does not require VCXO or SAW filter and meets CCITT G.958A specs.	\$17.80/1,000	Samples—Now Prod.—3Q95
IRM 3001 IRM3105	Siemens 408.257.7910	Two infrared transceivers conform to Irda specifications with data rates up to 115.2 Kbps, have shutdown mode less than 0.1-mA consumption.	\$4/1,000	Samples—2Q95 Prod.—3Q95
VG-469	Vadem 408.467.2100	PCMCIA controller is first chip to comply with PC Card '95 specification, with dual-voltage, DMA, and power-reduction support; in PQFP-208.	\$15/10,000	Prod.—Now
74ABTE16245 74ABTE16246	TI 800.477.8924	Enhanced transceiver logic (ETL) allows 16-bit buffers to support live insertion/withdrawal of VME boards in a running system.	\$4.35/1,000	Prod.—Now
LTC1279	Linear Technology 800.4LINEAR	A/D converter operates on 60 mW at 600 Ksamples/second with 12-bit accuracy from DC to Nyquist; in 24-lead SOIC package.	\$13/1,000	Prod.—Now
Memories				
IDT7015	IDT 800.345.7015	Dual-port RAM is organized as 8K×9 with access times as low as 17 ns, uses CMOS process; in PLCC-68, TQFP-80, or PGA-68 packages.	\$26.95/5,000	Prod.—Now
IDT7026 IDT70261	IDT 800.345.7015	Dual-port 16K×16 SRAMs are densest available, with access times as low as 20 ns; in PLCC, PGA, and TQFP packages.	\$39.95/5,000	Prod.—Now
IDT7MPV6200	IDT 800.345.7015	Asynchronous 256K cache SRAM module is designed for Pentium systems with Triton chip set.	\$75/10,000	Prod.—Now
CXK77V3210Q	Sony 408.432.0190	One-megabit synchronous burst SRAM is organized as 32K×32, supports pipeline and flow-through operation at 100 MHz.	\$31/100	Samples—Now Prod.—2Q95
TC55V1325FF TC55V1165FF	Toshiba 800.879.4963	Two low-power 1-Mbit synchronous SRAMs include 2-bit burst counters, self-timed write, organized as 32K×32 ('25) or 64K×16 ('65).	\$37/1	Samples—2Q95 Prod.—3Q95
Miscellaneous				
MSM665x	Oki 408.737.6445	Audio playback IC achieves superior voice reproduction using ADPCM for 3:1 compression, stores 127 phrases in on-chip ROM.	\$2.80/10,000	Prod.—Now
CY7B9910 CY7B9920	Cypress 408.943.2600	Low-skew clock buffers generate eight copies of input clock with less than 250 ps of skew and 500 ps of propagation delay at up to 80 MHz.	\$4.95/10,000	Prod.—Now
Programmable Logic				
EPM9560	Altera 408.894.7000	Largest member of MAX 9000 family of EPLDs has 560 macrocells, 216 I/O pins, 12,000 equivalent gates, and in-system programmability.	\$89/10,000	Prod.—Now