Message from the Heterogeneous Computing Workshop Steering Committee Chair

These are the proceedings of the 15th Heterogeneous Computing Workshop, also known as HCW 2006. Heterogeneous computing is a very important research area with great practical impact, and it includes a large range of systems. A heterogeneous computing system may be a set of machines interconnected by a wide-area network and used to support the execution of jobs submitted by a large variety of users to process data that is distributed throughout the system. It may be a suite of high-performance machines tightly interconnected by a fast, dedicated network and used to process a set of production tasks, where the communicating subtasks of each task may execute on different machines in the suite. It may also be a special-purpose embedded system, such as a set of different types of processors working together to perform a particular application. In one extreme, it may consist of a single machine that can reconfigure itself to operate in different ways (e.g., in different modes of parallelism). All of these types of heterogeneous computing systems (as well as others, e.g., grids and clusters) are appropriate topics for this workshop series. I hope you find the contents of these proceedings informative and interesting, and I encourage you to look also at the proceedings of past and future Heterogeneous Computing Workshops.

Many people have worked very hard to make this year's workshop happen. Jose A. B. Fortes, from the University of Florida, was this year's Program Committee Chair, and he assembled the excellent program for the workshop and collection of papers in these workshop proceedings. Jose did this with the assistance of his Program Committee, which is listed in these proceedings. Arnold L. Rosenberg, from the University of Massachusetts Amherst, was the General Chair. Arny was responsible for the overall organization and administration of this year's workshop, and he did a fine job. I thank Jose, Arny, and the Program Committee for their efforts. I also thank the workshop Steering Committee, listed in these proceedings, for their guidance and assistance.

The workshop is once again cosponsored by the IEEE Computer Society and the US Office of Naval Research. I thank the Office of Naval Research for their support of this workshop's proceedings.

This workshop is held in conjunction with the International Parallel and Distributed Processing Symposium (IPDPS), which is a merger of the symposia formerly known as the International Parallel Processing Symposium (IPPS) and the Symposium on Parallel and Distributed Processing (SPDP). The Heterogeneous Computing Workshop series is very appreciative of the cooperation and assistance we have received from the IPDPS/IPPS organizers for all of the workshop's 15 years.

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