

Distributed Computation in the Physical World

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Networks of intelligent sensors that are distributed through the physical world will revolutionize practices in the life sciences, civil engineering, manufacturing, security, agriculture, ubiquitous computing, and many other areas. They also present an opportunity and a need to explore distributed algorithms that are wedded to the noisy, localized, time varying physical world. Bandwidth, storage, and energy limitations make in-network processing essential – within the node and among collections of nodes. The algorithms should be resource efficient, but also deal with noise, uncertainty and dynamically changing connectivity. A broad research community has been exploring these issue in the context of TinyOS and the Berkeley motes. This talk will highlight novel distributed algorithms coming out of these efforts and discuss issues in making such networks robust and programmable.

BIO:

David E. Culler is a Professor of Computer Science at the University, where he has been on the faculty at Berkeley since 1989 and has served as Vice Chair for Industrial Relations and Vice Chair for Computing and Networking. He was founding director the Intel Research Lablet in Berkeley, which works in collaboration with the University. David received his B.A. from Berkeley in 1980, M.S. from MIT in 1985 and Ph.D. from MIT in 1989. He was selected in the Scientific American Top 50 Researchers and in Technology Reviews Top 10 Technologies that Will Change the World and awarded the NSF Presidential Young Investigator, NSF Presidential Faculty Fellowship, and ACM Fellow. His research addresses vast networks of small, embedded wireless devices, internet service architectures, parallel computer architecture, parallel programming languages, and high performance communication. He is well known for his work on TinyOS, PlanetLab, Networks of Workstations (NOW), Internet services, Active Messages, Split-C, the Threaded Abstract Machine (TAM), and dataflow systems. He has published widely in leading conferences and journals, co-authored a graduate text on Parallel Computer Architecture, obtained three patents and has served as program-chair for several leading conferences.