

WELCOME

Welcome to the Big Apple for the 12-th Great Lakes VLSI Symposium. While New York City is not exactly on the Great Lakes, it does have a connection to Lake Ontario through the Hudson and the old waterways. This justifies the choice of the Big Apple as the venue for this year's GLSVLSI! Once again this symposium has attracted an excellent assortment of papers, over a range of topics that are fundamental to advancing the state of the art.

This years program has been carefully selected by the program co-chairs through peer review, with each paper getting at least three reviews, in a record review period of just one month. Our kudos go to the program committee members and additional reviewers for completing their hard work in such a short time. We believe that we have a strong and interesting selection of papers for this symposium covering all major aspects of VLSI design.

This year, we received 71 uniformly high quality submissions. We would like to thank all the authors who submitted their manuscripts for consideration. The technical program committee had great difficulty in limiting the number of accepted papers to fit time constraints of the conference. Of the submitted papers, 19 were accepted as full papers, 12 as short papers, and an additional 10 as posters. Approximately half of the full and short papers cover some aspect of VLSI CAD. A third discuss circuit related topics, and another third cover specific chip, subsystem, or system design. A tenth address topics of a more theoretical nature, while a full 25% touch on the increasingly important area of low power. From our perspective, this is a very satisfying mix.

We are very grateful to Dr. Shekhar Borkar of Intel for agreeing to serve as the keynote speaker for presenting what is surely to be a very stimulating and visionary talk.

We would like to thank the Association of Computing Machinery and SigDA for serving as the primary sponsors of this conference. We are also thankful for the support of a number of other organizations: the Integrated Electronics Engineering Center at SUNY-Binghamton (Dr. Bahgat Sammakia), the NYSTAR-funded Microelectronics Design Center at the University of Rochester (Prof. Eby Friedman), the Department of Computer Science at SUNY-Binghamton and the Thomas J. Watson School of Engineering and Applied Sciences, SUNY-Binghamton.

The management of this conference was greatly facilitated by the services of the web masters, Mehmet Can Yildiz and Deger Cenk Erdil. Systems support for the symposium was provided by Stea Greene. Without the assistance of the dedicated staff at the ACM, particularly, Donna Baglio, Ann Ferrara and Maritza Nichols, symposiums like this one become impossible. Prof. Jim Cohoon of the University of Virginia and Prof. Kaushik Roy of Purdue provided us with many valuable suggestions to make our tasks easier. Last, but not the least, we would like to thank Lisa Tolles-Efinger from Sheridan Printing and Kathy Preas from KP Publications for producing the printed and electronic versions of the proceedings.

Kanad Ghose, General Chair
Patrick H. Madden, Vice-General Chair

Vivek De, Program Co-Chair
Peter M. Kogge, Program Co-Chair