MESSAGE FROM THE STEERING COMMITTEE CHAIR

It gives me great pleasure to welcome all of you to the Eight Great Lakes Symposium on VLSI on behalf of the Steering committee. The idea for GLSVLSI was conceived in 1989, and we started the symposium in 1991. The purpose of the symposium, has been and will remain, to showcase the latest international research in various fields of VLSI to a midwest audience. This area is not served by any major VLSI conference. The Great Lakes Symposium is designed to provide a research type atmosphere to attend, present, contribute, learn and teach others. There are no high-pressure presentations nor do we have any trade shows. This tradition continues in this symposium as we continue to strive for excelence in our ideal in research oriented symposiums.

The overall conference is enlarged this year largely due to untiring efforts of Professor Magdy A. Bayoumi. Steering Committee would like to thank him for taking the conference a new level of participation and excellence. We also sincerely thank Cathy Pomier and students of Professor Bayoumi for their efforts in conference organization.

The technical program committee has done an excellent job in selecting high quality papers and setting a solid technical program. I sincerely thank Professor Graham Jullien for his hard work and dedication. Thanks are also due to the program committee members for their efforts.

I thank all the authors who have contributed the conference. Thanks are also due to reviewers, who got all the papers reviewed in time. I hope that all of you will submit papers and contribute to the GLSVLSI '99 as well.

I thank IEEE Circuits and Systems and IEEE Computer Society for co-sponsoring this symposium.

Sincerely,

Naveed Sherwani

Chair, Steering Committee for Great Lakes Symposium on VLSI. Naveed Sherwani can be reached at sherwani@ichips.intel.com