

PANEL: GEARING UP FOR THE TECHNOLOGY EXPLOSION

Chair: Gary Smith - Dataquest, San Jose, CA

Organizer: Marion Kenefick - Viewlogic Systems, Inc., Los Gatos, CA

Predictions are that over the next decade demands for advanced electronic equipment will create a deficit in the fab capacity, engineering talent, and CAD tools needed to meet high abstraction, large gate count, deep submicron designs. This increased demand is largely due to the use of compact electronics in consumer products. EDA tools play a key role in the technology explosion; much of what is taken for granted today in the electronics marketplace would be too expensive, too time-consuming to design, or too complex to produce without design automation. Understanding technology trends, where to invest, how to gear up new designs quickly and how to re-use key design blocks to cut design time is key to keeping ahead.

This panel will explore key technology trends with leading technologists from the user and design automation industries.

Panel Members:

Walt Davis - Motorola, Boynton Beach, FL

Glenn House - Mentor Graphics Corp., Wilsonville, OR

Kurt Keutzer - Synopsys, Inc., Mountain View, CA

Jim Pena - LSI Logic, Milpitas, CA

Craig Peterson - Intel Development Labs., Hillsborough, OR

Lawrence Rubin - Viewlogic Systems, Inc., Camarillo, CA

Jim Solomon - Cadence Design Systems, Inc., San Jose, CA