

ASP-DAC 2007 General Chair's Message

On behalf of the Organizing Committee, I would like to welcome you to the Asia and South Pacific Design Automation Conference 2007 (ASP-DAC 2007) being held here at Pacifico Yokohama, Japan, from January 23 through 26, 2007, jointly with the Electronic Design and Solution Fair 2007.

As we are going into the era of nano-scale integrated circuits, many issues will confront us such as complexity, manufacturability, and power dissipation. These issues can be overcome by tighter collaboration than ever among EDA researchers, designers, manufacturers, and application engineers. ASP-DAC 2007 offers an ideal place for all these people to meet and exchange ideas about the challenges and solutions for the future.

The core of the conference is the technical program. This year, ASP-DAC received 408 submissions from 30 countries/regions. Based on the result of a rigorous and thorough review followed by a full day face-to-face discussion, 131 papers were selected and compiled into an exciting final program which is further enriched by multiple special sessions and panels.

Each day, the technical program starts with a keynote address. On Wednesday, Prof. Rob A. Rutenbar, Carnegie Mellon University, will explore next-generation design and EDA challenges. On Thursday, Prof. Takayasu Sakurai, University of Tokyo, will discuss fundamental issues in nano-meter CMOS design. On Friday, Dr. Fu-Chieh Hsu, TSMC, will explain their effort for improving process data accuracy.

Last year, ASP-DAC successfully launched a new program called Designers' Forum that shares design experience and solutions of real product designs of the industries. This year's program includes oral sessions of low-power design and high-speed signaling, panels of top 10 design issues and HW/SW verification.

The University Design Contest is also an important annual event of ASP-DAC where 18 designs were selected for presentation. On Tuesday, two full-day and four half-day tutorials are scheduled to provide introductions to hot topics such as DFM, low-power, verification, physical design, and embedded system design.

An event like ASP-DAC doesn't just happen. We wish to express our appreciation to all authors, speakers, reviewers, session organizers, session chairs, panelists and keynote speakers. Also my sincere thanks to the members of the Organizing Committee, the Technical Program Committee, the University LSI Design Contest Committee, the Industry Liaison and the Steering Committee.

Finally, special thanks to all ASP-DAC attendees — we are sure you will have a productive and exciting experience at ASP-DAC 2007.



A handwritten signature in black ink that reads "H. Onodera". The signature is written in a cursive, slightly slanted style.

Hidetoshi Onodera
General Chair
ASP-DAC 2007