

ASP-DAC 2006 Best Papers

Best Paper Award

- 1B-1 **Constraint-Driven Bus Matrix Synthesis for MPSoC**
Sudeep Pasricha, Nikil Dutt (Univ. of California, Irvine, United States), Mohamed Ben-Romdhane (Conexant, United States)
- 3C-1 **Post-Routing Redundant Via Insertion for Yield/Reliability Improvement**
Kuang-Yao Lee, Ting-Chi Wang (National Tsing Hua Univ., Taiwan)

Best Paper Candidates

- 1B-1 **Constraint-Driven Bus Matrix Synthesis for MPSoC**
Sudeep Pasricha, Nikil Dutt (Univ. of California, Irvine, United States), Mohamed Ben-Romdhane (Conexant, United States)
- 3C-1 **Post-Routing Redundant Via Insertion for Yield/Reliability Improvement**
Kuang-Yao Lee, Ting-Chi Wang (National Tsing Hua Univ., Taiwan)
- 7B-1 **Equivalent Circuit Modeling of Guard Ring Structures for Evaluation of Substrate Crosstalk Isolation**
Daisuke Kosaka, Makoto Nagata (Kobe Univ., Japan)
- 7C-2 **Speed Binning Aware Design Methodology to Improve Profit under Parameter Variations**
Animesh Datta (Purdue Univ., United States), Swarup Bhunia (Case Western Reserve Univ., United States), Jung Hwan Choi, Saibal Mukhopadhyay, Kaushik Roy (Purdue Univ., United States)
- 8A-1 **Fast Substrate Noise-Aware Floorplanning with Preference Directed Graph for Mixed-Signal SOCs**
Minsik Cho, Hongjoong Shin, David Z. Pan (Univ. of Texas, Austin, United States)
- 8B-1 **Finding Optimal L1 Cache Configuration for Embedded Systems**
Andhi Janapsatya, Aleksandar Ignjatovic, Sri Parameswaran (Univ. of New South Wales, Australia)
- 9A-1 **TAPHS: Thermal-Aware Unified Physical-Level and High-Level Synthesis**
Zhenyu (Peter) Gu (Northwestern Univ., United States), Yonghong Yang (Queen's Univ., Canada), Jia Wang, Robert P. Dick (Northwestern Univ., United States), Li Shang (Queen's Univ., Canada)
- 9C-3 **Statistical Leakage Minimization through Joint Selection of Gate Sizes, Gate Lengths and Threshold Voltage**
Sarvesh Bhardwaj, Yu Cao, Sarma Vrudhula (Arizona State Univ., United States)

Design Contest Award

Outstanding Design Award

1D-15 A 52mW 1200MIPS Compact DSP for Multi-Core Media SoC

*Shih-Hao Ou, Tay-Jyi Lin, Chao-Wei Huang, Yu-Ting Kuo, Chie-Min Chao, Chih-Wei Liu
(National Chiao Tung Univ., Taiwan), Chein-Wei Jen (STC, ITRI, Taiwan)*

Special Feature Award

1D-1 A Low Dynamic Power and Low Leakage Power 90-nm CMOS Square-Root Circuit

Tadayoshi Enomoto, Nobuaki Kobayashi (Chuo Univ., Japan)

1D-8 Adaptively-Biased Capacitor-Less CMOS Low Dropout Regulator with Direct Current Feedback

Yat-Hei Lam, Wing-Hung Ki, Chi-Ying Tsui (Hong Kong Univ. of Science and Tech., Hong Kong)